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500.39590X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. MIYAKI, et al.
Application No.: 09/785,452
Filed: February 20, 2001
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND METHOD OF MANUFACTURING THE SAME
Art Unit: 2827
Examiner: L. Cruz

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 24, 2003

Sir:

In response to the Office Action dated August 27, 2003, Applicants respectfully submit that at least claims 2-7, 9 and 10 are clearly readable on the elected Species 1 (e.g. Fig. 13). Accordingly, examination on the merits with regard to claims 2-7, 9 and 10 is respectfully requested.

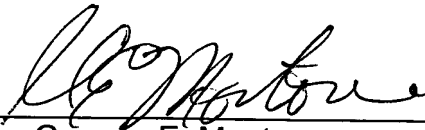
If the Examiner believes that there are any other points which may be clarified or otherwise disposed of either by telephone discussion or by personal interview, the Examiner is invited to contact Applicants' undersigned attorney at the number indicated below.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135

(Case No. 500.39590X00), and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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By 
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GEM/dlt

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